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Docket No.: M4065.0852/P852
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Peter P. Altice, Jr. et al.

Application No.: 10/721,190

Confirmation No.: 6553

Filed: November 26, 2003

Art Unit: 2612

For: CMOS IMAGER WITH A CAPACITIVE
STORAGE NODE LINKED TO TRANSFER
GATE

Examiner: Not Yet Assigned

STATUS INQUIRY

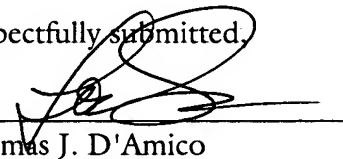
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive an Office Action from the Patent and Trademark Office.

Dated: December 9, 2004

Respectfully submitted,

By 
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